

| L Number | Hits | Search Text | DB | Time stamp |
|----------|---------|---|---|------------------|
| 4 | 14 | (macro\$6 or cell) same library same gdsii | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/02 16:23 |
| - | 48754 | ("integrated circuit" or IC) same design\$3 | USPAT | 2004/07/22 16:32 |
| - | 37081 | (acertain\$3 or determin\$5) same function same specific\$5 | USPAT | 2004/07/22 16:32 |
| - | 5904 | map\$5 same function same specific\$5 | USPAT | 2004/07/22 16:32 |
| - | 24354 | (acertain\$3 or determin\$5) same function same (placement or location) | USPAT | 2004/07/22 16:32 |
| - | 80 | ((("integrated circuit" or IC) same design\$3) and ((acertain\$3 or determin\$5) same function same specific\$5) and (map\$5 same function same specific\$5) and ((acertain\$3 or determin\$5) same function same (placement or location))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 6317675 | block based design | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 163 | block same design same placement same interconnection | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 1 | ((block same design same placement same interconnection) same function)and GDSII | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 1 | (block same design same placement same interconnection) and GDSII | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 1619 | block same design same placement | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 208526 | layers same vias | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 80014 | clock same power | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 421 | layers same clock same power | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 1432 | clock same power same layer | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |

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| - | 13080 | rout\$3 same different same (level or layer) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 1167 | 716/\$.ccls. and (block same size) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 1529 | 716/\$.ccls. and (block same gate) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 782 | 716/\$.ccls. and (block same transistor) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 22 | ((("integrated circuit" or IC) same design\$3) and (acertain\$3 or determin\$5) same function same specific\$5) and (map\$5 same function same specific\$5) and (acertain\$3 or determin\$5) same function same (placement or location))) and 716/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 51 | "block based design" | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 5 | "block based design" and (size same shape) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 2352 | (716/\$.ccls. and (block same size)) or (716/\$.ccls. and (block same gate)) or (716/\$.ccls. and (block same transistor)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 1040 | ((716/\$.ccls. and (block same size)) or (716/\$.ccls. and (block same gate)) or (716/\$.ccls. and (block same transistor))) and hierarch\$6 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 27 | (block same design same placement same interconnection) same function | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:33 |
| - | 971 | ((716/\$.ccls. and (block same size)) or (716/\$.ccls. and (block same gate)) or (716/\$.ccls. and (block same transistor))) and hierarch\$6 and function | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 1840 | (716/\$.ccls. and (block same gate)) or (716/\$.ccls. and (block same transistor)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |

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| - | 267 | GDSII | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 348 | thousand adj gate | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 79 | (block same design same placement) and (layers same vias) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 10 | (block same design same placement) and trailer | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 496 | small near5 function\$2 near5 block | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 597 | small near5 function\$2 near5 (block or core) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 7901 | 716/\$.ccls. (small near5 function\$2 near5 block) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 75 | 716/\$.ccls. and (small near5 function\$2 near5 (block or core)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 65 | (block same design same placement) and (glue near3 logic) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 16 | (block same design same placement) and (layers same clock same power) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:35 |
| - | 36 | (clock same power same layer) and (block same design same placement) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 1472 | glue adj logic | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |

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| - | 2094 | (power same layer) and (clock same layer) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 2517 | (power same layer) and (clock same layer) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 1432 | (power same layer) same (clock same layer) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 156 | (block same design same placement) and (route\$3 same different same (level or layer)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 89 | ((power same layer) and (clock same layer)) and 716/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 3 | 09/098,599 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 1 | 716/\$.ccls. and ("data path" same memory same control same i/o same analog) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 1 | 716/\$.ccls. and ("data path" same memory same control same i/o same analog same group) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 87 | "1000 gates" | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 62 | (((((716/\$.ccls. and (block same size)) or (716/\$.ccls. and (block same gate)) or (716/\$.ccls. and (block same transistor)))) and hierarch\$6) and function) and(trailer or "glue logic") | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 68 | ((716/\$.ccls. and (block same gate)) or (716/\$.ccls. and (block same transistor))) and (trailer or "glue logic") | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 113 | 716/\$.ccls. and sub-block | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |

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| - | 65 | (thousand adj gate) same block | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 39 | 716/\$.ccls. and ((thousand adj gate) same block) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 8 | 716/8.ccls. and (thousand adj gate) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 77 | 716/\$.ccls. and (thousand adj gate) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 71 | 716/\$.ccls. and (small near5 function\$2 near5 block) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 87 | (glue adj logic) and 716/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:34 |
| - | 1 | "5859999".PN. | USPAT | 2004/07/22 16:34 |
| - | 76 | ((power same layer) same (clock same layer)) and 716/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:35 |
| - | 1 | "6014507".PN. | USPAT | 2004/07/22 16:35 |
| - | 1 | "5984510".PN. | USPAT | 2004/07/22 16:35 |
| - | 1 | "5860017".PN. | USPAT | 2004/07/22 16:35 |
| - | 1 | "5841664".PN. | USPAT | 2004/07/22 16:35 |
| - | 22883 | (block or group or macro\$5) same library | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:37 |
| - | 1261 | ((block or group or macro\$5) same library) and 716/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:37 |
| - | 660 | (block or group or macro\$5) same library same layers | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:37 |
| - | 100 | ((block or group or macro\$5) same library same layers) and 716/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/29 14:33 |

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| - | 81 | ((block or macro\$5) same library same layers) and 716/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/29 14:34 |
| - | 30 | ((block or macro\$5) same library same layers) and 716/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/29 14:35 |
| - | 33 | ((block or macro\$5) same librar\$4 same layers) and 716/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/29 14:35 |
| - | 14 | (macro\$5 same librar\$4 same layers) and 716/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/29 14:41 |
| - | 72 | (macro\$5 same (wir\$5 or intercon\$8) same layers) and 716/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/29 15:49 |
| - | 0 | macro\$5 same netlist same layout same pin same speed | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/29 15:52 |
| - | 59 | macro\$5 same netlist same layout | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/29 15:54 |
| - | 9 | macro\$5 same netlist same layout same hard | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/29 16:38 |
| - | 1 | "5883814".PN. | USPAT | 2004/07/29 15:57 |
| - | 25 | cell same netlist same layout same hard | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/29 16:39 |
| - | 13 | cell same netlist same layout same hard same (connec\$5 or pin) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/02 14:47 |
| - | 17 | "5880971" | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/02 14:52 |
| - | 0 | "seemingly insurmountable job of designing a highly complex circuit is broken into small, workable design projects" | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/02 14:53 |

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| - | 3 | "seemingly insurmountable job" | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/02 16:00 |
| - | 109 | (macro\$6 or cell) same library same different same size same shape | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/02 16:02 |
| - | 0 | ((macro\$6 or cell) same library same different same size same shape) and 716/\$.ccls.) and gdsiii | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/02 16:04 |
| - | 1 | GDSIII | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/02 16:03 |
| - | 270 | GDSII | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/02 16:03 |
| - | 1 | ((macro\$6 or cell) same library same different same size same shape) and 716/\$.ccls.) and gdsii | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/02 16:23 |
| - | 65 | ((macro\$6 or cell) same library same different same size same shape) and 716/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/02 16:05 |